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 Chiu, J.-C.; Chung, C.-P.;
[Computers and Digital Techniques, IEE Proceedings-](#)
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- ☐ 3. **An empirical study of data speculation use on the Intel Itanium 2 processor**
 Mock, M.; Villamarin, R.; Balocchi, J.;
[Interaction between Compilers and Computer Architectures, 2005. INTERACT-9. 9th Annual Work-](#)
 13 Feb. 2005 Page(s):22 - 33
 Digital Object Identifier 10.1109/INTERACT.2005.2
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 Keller, H.; Favre, A.; Comazzi, A.;
[Acoustics, Speech, and Signal Processing, IEEE International Conference on ICASSP '82.](#)
 Volume 7, May 1982 Page(s):1191 - 1194
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- ☐ 5. **Design and Implementation of a high speed microprocessor simulator BurstScalar**
 Nakada, T.; Nakashima, H.;
[Modeling, Analysis, and Simulation of Computer and Telecommunications Systems, 2004. \(MASCOT\)](#)
[Proceedings, The IEEE Computer Society's 12th Annual International Symposium on](#)
 4-8 Oct. 2004 Page(s):364 - 372
 Digital Object Identifier 10.1109/MASCOT.2004.1348291
[AbstractPlus](#) | Full Text: [PDF](#)(905 KB) IEEE CNF

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- ☐ 6. **Low cost instruction cache designs for tag comparison elimination**
Youtao Zhang; Jun Yang;
[Low Power Electronics and Design, 2003. ISLPED '03. Proceedings of the 2003 International Symposium](#)
25-27 Aug. 2003 Page(s):266 - 269
Digital Object Identifier 10.1109/LPE.2003.1231874
[AbstractPlus](#) | Full Text: [PDF](#)(563 KB) IEEE CNF
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- ☐ 7. **The iCORE/spl trade/ 520 MHz synthesizable CPU core**
Richardson, N.; Lun Bin Huang; Hossain, R.; Zounes, T.; Soni, N.; Lewis, J.;
[Design Automation Conference, 2002. Proceedings, 39th](#)
10-14 June 2002 Page(s):640 - 645
Digital Object Identifier 10.1109/DAC.2002.1012703
[AbstractPlus](#) | Full Text: [PDF](#)(656 KB) IEEE CNF
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- ☐ 8. **On some implementation issues for value prediction on wide-issue ILP processors**
Sang-Jeong Lee; Pen-Chung Yew;
[Parallel Architectures and Compilation Techniques, 2000. Proceedings, International Conference on](#)
15-19 Oct. 2000 Page(s):145 - 156
Digital Object Identifier 10.1109/PACT.2000.888339
[AbstractPlus](#) | Full Text: [PDF](#)(1048 KB) IEEE CNF
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- ☐ 9. **Looking at history to filter allocations in prediction tables**
Morancho, E.; Liaberia, J.M.; Olive, A.;
[Parallel Architectures and Compilation Techniques, 1999. Proceedings, 1999 International Conference on](#)
12-16 Oct. 1999 Page(s):314 - 319
Digital Object Identifier 10.1109/PACT.1999.807577
[AbstractPlus](#) | Full Text: [PDF](#)(84 KB) IEEE CNF
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- ☐ 10. **Accurate indirect branch prediction**
Driesen, K.; Holzle, U.;
[Computer Architecture, 1998. Proceedings, The 25th Annual International Symposium on](#)
27 June-1 July 1998 Page(s):167 - 178
Digital Object Identifier 10.1109/ISCA.1998.694772
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- ☐ 11. **Analyzing the working set characteristics of branch execution**
Kim, S.P.; Tyson, G.S.;
[Microarchitecture, 1998. MICRO-31. Proceedings, 31st Annual ACM/IEEE International Symposium on](#)
30 Nov.-2 Dec. 1998 Page(s):49 - 58
Digital Object Identifier 10.1109/MICRO.1998.742768
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- ☐ 12. **Compiler-directed early load-address generation**
Ben Chung Cheng; Connors, D.A.; Hwu, W.W.;
[Microarchitecture, 1998. MICRO-31. Proceedings, 31st Annual ACM/IEEE International Symposium on](#)
30 Nov.-2 Dec. 1998 Page(s):138 - 147
Digital Object Identifier 10.1109/MICRO.1998.742776
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**13. An X86 microprocessor with multimedia extensions**

Draper, D.A.; Crowley, M.P.; Holst, J.; Favor, G.; Schoy, A.; Ben-Meir, A.; Trull, J.; Khanna, R.; We Nolan, J.; Partovi, H.; Johnson, M.; Lee, T.; Mallick, D.; Frydel, G.; Vuong, A.; Yu, S.; Maley, R.; Ka
Solid-State Circuits Conference, 1997. Digest of Technical Papers. 44th ISSCC., 1997 IEEE Intern
6-8 Feb. 1997 Page(s): 172 - 173, 450
Digital Object Identifier 10.1109/ISSCC.1997.585321

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**14. Optoelectronic look-up table using VCSEL-based logic**

Mitkas, P.A.; Irakliotis, L.J.; Beyette, F.R., Jr.; Feld, S.A.; Wilmsen, C.W.;
Lasers and Electro-Optics Society Annual Meeting, 1993. LEOS '93 Conference Proceedings, IEEE
15-18 Nov. 1993 Page(s): 71 - 72
Digital Object Identifier 10.1109/LEOS.1993.379121

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